

Electronically filed November 29, 2012

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent of:	Chalfin et al.	Confirmation No.:	1891
Patent No.:	6,879,948	Issued:	April 12, 2005
Serial No.:	09/459,995	Filed:	December 14, 1999
For:	Synchronization of Hardware Simulation Processes	Attorney Docket No.:	001361-5064-US

STATEMENT UNDER 37 C.F.R. § 3.73(b)

Commissioner for Patents
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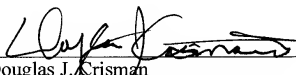
Sir:

Silicon Graphics International Corp., a corporation, states that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of a chain of assignments in the instant application as follows:

- | | | | |
|-----------|--|-------|-------------------------|
| From: | Alex Chalfin, Jeffrey Daudel, Mark Grossman, Shrijeet Mukherjee, Peter Ostrin and Jarrett Redd | | |
| To: | Silicon Graphics, Inc. | | |
| Recorded: | December 14, 1999 | Reel: | 010462 Frame: 0538 |
- | | | | |
|-----------|--------------------------------------|-------|-------------------------|
| From: | Silicon Graphics, Inc. | | |
| To: | Silicon Graphics International Corp. | | |
| Recorded: | February 16, 2012 | Reel: | 027727 Frame: 0020 |

The undersigned is authorized to act on behalf of the assignee.

Date: November 29, 2012

 39,951

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